

SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY
Ichchhanath, Surat-395007 (Gujarat), India

REPORT SHEET SHOWING RESULT OF CANDIDATES APPEARED AT M.TECH. I SECOND SEMESTER VLSI AND EMBEDDED SYSTEMS AT REGULAR EXAM HELD DURING APRIL/MAY 2025

					1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark
Semester	Student Name		Course Code		ECVL102 T	ECVL104 T	ECVL106 P	ECVL108 P	ECVL116 T	ECVL118 T	ECVL178 T								
			Credit		3.00	3.00	3.00	2.00	3.00	3.00	3.00								
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	ANALOG VLSI DESIGN	REAL-TIME SYSTEMS	VLSI LAB - II	MINI PROJECT	VLSI SYSTEM DESIGN (CE-IV)	SOC DESIGN (CE-V)	FUNDAMENTALS OF SEMICONDUCTOR PACKAGE MANUFACTURING AND TEST (EIS-I)								
Semester : 2 RITOBHRATA MANDAL					AB	AB	AB	AB	AB	BB	AB								
P24VL001 20.00 20.00 8.85 8.55																			
Semester : 2 MUSKAN SRIVASTAVA					BC	BC	BB	AB	BC	CC	BB								
P24VL002 20.00 20.00 7.35 7.20																			
Semester : 2 DOKALA V V SRI PAVAN KUMAR					AB	BB	BB	AB	AA	CC	BB								
P24VL003 20.00 20.00 8.25 8.35																			
Semester : 2 GOWTHAM RAJ KHANDAVALLI					BB	CC	AB	BB	CD	BC	BB								
P24VL004 20.00 20.00 7.25 7.25																			
Semester	Student Name		Course Code		ECVL102 T	ECVL104 T	ECVL106 P	ECVL108 P	ECVL116 T	ECVL118 T	ECCS172 T								
			Credit		3.00	3.00	3.00	2.00	3.00	3.00	3.00								
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	ANALOG VLSI DESIGN	REAL-TIME SYSTEMS	VLSI LAB - II	MINI PROJECT	VLSI SYSTEM DESIGN (CE-IV)	SOC DESIGN (CE-V)	DEEP LEARNING, THEORY AND PRACTICE (EIS-I)								
Semester : 2 RAJA BHARGAV					BB	BB	AA	AA	AB	AB	BB								
P24VL005 20.00 20.00 8.80 8.88																			

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Semester	Student Name		Course Code		ECVL102 T	ECVL104 T	ECVL106 P	ECVL108 P	ECVL116 T	ECVL118 T	ECVL178 T								
				Credit	3.00	3.00	3.00	2.00	3.00	3.00	3.00								
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	ANALOG VLSI DESIGN	REAL-TIME SYSTEMS	VLSI LAB - II	MINI PROJECT	VLSI SYSTEM DESIGN (CE-IV)	SOC DESIGN (CE-V)	FUNDAMENTALS OF SEMICONDUCTOR PACKAGE MANUFACTURING AND TEST (EIS-I)								
Semester : 2 AMAN PATEL					BB	BC	BB	AB	CC	DD	BC								
P24VL006 20.00 20.00 6.90 7.05																			
Semester : 2 M VENKATESHWARA RAO					BC	BC	BB	AB	BC	BB	AB								
P24VL007 20.00 20.00 7.80 7.80																			
Semester : 2 SAURAV KUMAR					AB	BB	AB	AB	AA	BC	AA								
P24VL008 20.00 20.00 8.85 8.63																			
Semester : 2 DHANORKAR ARYA DHANANJAY ANITA					BB	CC	BB	AB	CC	BB	BB								
P24VL009 20.00 20.00 7.50 7.78																			
Semester : 2 AKASH ARUN					AB	BC	AB	AB	BC	BB	AB								
P24VL010 20.00 20.00 8.25 8.40																			
Semester : 2 OMMI SRILEKHA					BC	CC	BC	BB	BC	CD	BB								
P24VL011 20.00 20.00 6.80 6.65																			
Semester : 2 LAKKIMSETTI YESHWANTH					BB	BC	BB	AB	BB	BB	BB								
P24VL012 20.00 20.00 7.95 7.35																			
Semester : 2 YENIKA LATHA SRI					CC	CD	BC	BB	CC	CC	BC								
P24VL013 20.00 20.00 6.35 6.00																			
Semester : 2 UMANG MAHENDRABHAI JOSHI					BB	BC	AB	BB	BB	BC	BB								
P24VL014 20.00 20.00 7.85 7.43																			
Semester : 2 CHIKKU NARESH					BC	BC	BB	AB	BB	BC	AB								
P24VL015 20.00 20.00 7.80 8.33																			
Semester : 2 PATEL BRIJESH RAMANBHAI					AA	BB	AA	BB	AA	AA	AA								
P24VL016 20.00 20.00 9.50 9.28																			
Semester : 2 SINOJIYA DIPEN SHASHIKANTBHAI					BB	CC	AB	AA	CD	AA	AA								
P24VL017 20.00 20.00 8.20 8.10																			
Semester : 2 SEERAPU VISWESWARARAO					BB	CD	AB	BB	BB	AB	BB								
P24VL018 20.00 20.00 7.85 8.15																			
Semester : 2 KUNISETTI VISHNU VARDHAN					AB	BC	AA	BB	AB	BB	AB								
P24VL019 20.00 20.00 8.60 8.43																			
Semester : 2 GUNDREDIPALLY SURAJ					BB	BB	BB	AB	AB	CC	BB								
P24VL020 20.00 20.00 7.95 7.43																			
Semester : 2 RUDRA UDAY KIRAN					AB	BB	BB	AB	AB	BC	BB								
P24VL021 20.00 20.00 8.25 8.08																			

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Semester	Student Name				Course Code	ECVL102 T	ECVL104 T	ECVL106 P	ECVL108 P	ECCS102 T	ECVL118 T	ECCS172 T							
					Credit	3.00	3.00	3.00	2.00	3.00	3.00	3.00							
	Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	ANALOG VLSI DESIGN	REAL-TIME SYSTEMS	VLSI LAB - II	MINI PROJECT	IMAGE PROCESSING AND COMPUTER VISION (CE-IV)	SOC DESIGN (CE-V)	DEEP LEARNING, THEORY AND PRACTICE (EIS-I)							
Semester : 2 JARULLAH MUHAMMED UMAIR MUHAMMED UZAIR						BB	AB	BB	AA	AB	CC	BC							
P24VL022 20.00 20.00 8.05 8.23																			
Semester	Student Name				Course Code	ECVL102 T	ECVL104 T	ECVL106 P	ECVL108 P	ECVL116 T	ECVL118 T	ECVL178 T							
					Credit	3.00	3.00	3.00	2.00	3.00	3.00	3.00							
	Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	ANALOG VLSI DESIGN	REAL-TIME SYSTEMS	VLSI LAB - II	MINI PROJECT	VLSI SYSTEM DESIGN (CE-IV)	SOC DESIGN (CE-V)	FUNDAMENTALS OF SEMICONDUCTOR PACKAGE MANUFACTURING AND TEST (EIS-I)							
Semester : 2 KONDREDDY VIJAY SIMHA REDDY						BB	BB	BC	BB	BC	BB	BC							
P24VL023 20.00 20.00 7.55 7.40																			
Semester : 2 OBI REDDY HARSHAVARDHAN REDDY						BB	AB	AB	BB	BC	BC	BB							
P24VL024 20.00 20.00 8.00 8.15																			
Semester : 2 BUYYANA NEERAJ						BB	BB	BB	AB	BC	AB	AB							
P24VL025 20.00 20.00 8.25 8.50																			
Semester : 2 D SHASHIKANTH						BC	CC	BC	AB	BC	CC	BB							
P24VL026 20.00 20.00 7.05 6.83																			
Semester : 2 POLIMARI EKANTH VARDHAN						BB	CC	BC	BB	BC	BB	AB							
P24VL027 20.00 20.00 7.55 7.63																			
Semester : 2 SHIVAM KUMAR						AB	BC	BB	BB	BB	AB	AA							
P24VL028 20.00 20.00 8.45 8.65																			
Semester : 2 SHIVAM PANDEY						BC	CD	BB	AB	BB	BB	BB							
P24VL029 20.00 20.00 7.50 7.20																			
Semester : 2 KATRAVATH GOVIND						CC	CD	BB	BB	BB	FF	CD							BACKLOG : 3 CREDIT(S)
P24VL030 20.00 17.00 5.60 6.05																			

Total Student Count : 30